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This set of computer organization and architecture objective questions include the collections of objective questions on the fundamentals of computer organization and architecture. 1. Memory unit that communicates directly with the CPU is called the A) Main memory B) Secondary memory C) Auxiliary memory D) Register. 2.

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s are sequenced before the lines in set (s+1). The main memory blocks are numbered 0 onwards.

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computer organization and architecture . You are developing a high frequency trading system to try to beat the stock market. Your system will require a very high hit rate with low hit times in a medium-sized cache between main memory and the CPU. Because of the almost unlimited budget your firm has, the hit rate is prioritized over all other ...

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2. SIMD represents an organization that _____. (A) refers to a computer system capable of processing several programs at the same time. (B) represents organization of single computer containing a control unit, processor unit and a memory unit. (C) includes many processing units under the supervision of a common control unit (D) none of the above.

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In computer engineering, computer

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architecture is a set of rules and methods that describe the functionality, organization, and implementation of computer systems. Some definitions of architecture define it as describing the capabilities and programming model of a computer but not a particular implementation. In other definitions computer architecture involves instruction set architecture design, microarchitecture design, logic design, and implementation.

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course is intended to teach the basics involved in data representation and digital logic circuits used in the computer system. This includes the general concepts in digital logic design, including logic elements, and their use in combinational and sequential logic circuit design.

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~~L 1.1: Computer Organization and Architecture Syllabus ...~~

Course Description & Objectives: Computer architecture is a key component of computer engineering and the practicing computer engineer should have a practical understanding of this topic. It is concerned with all aspects of the design and organization of the central processing unit and the integration of the CPU into the computer system itself

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